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Applicant(s)

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Examiner Name

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U.S. PATENT DOCUMENTS


EXAMINER INITIALS	DOCUMENT NUMBER	ISSUE DATE	PATENTEE	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	DOCUMENT NUMBER	PUBLICATION DATE	COUNTRY OR PATENT OFFICE	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS

EXAMINER INITIALS	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
N	Joshi, Robindra B. et al., "WP 4.2: A 100MHz, 5MBaud QAM Decision-Feedback Equalizer for Digital Television Applications" 1994 IEEE International Solid-State Circuits Conference, ISSCC94/Session 4/Video and Communication Signal Processors/Paper WP 4.2 pp. 68-69
N	Martin, James et al., "Local Area Networks Architectures and Implementations" Second Edition, 1994, pp. 167-170, 204
N	Razavi, Behzad, "Monolithic Phase-Locked Loops and Clock Recovery Circuits - Theory and Design" IEEE Press, 1996, Tutorial pp. 2-7
N	Anderson, John B., "Digital Transmission Engineering" IEEE Press, 1999, Synchronization - Chapter 4, pp. 193-196

EXAMINER SIGNATURE	DATE CONSIDERED
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